SERIAL NO.: 10/612,722



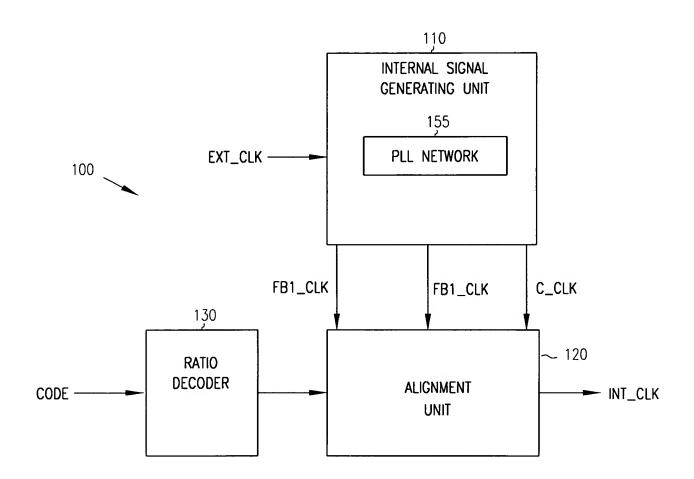
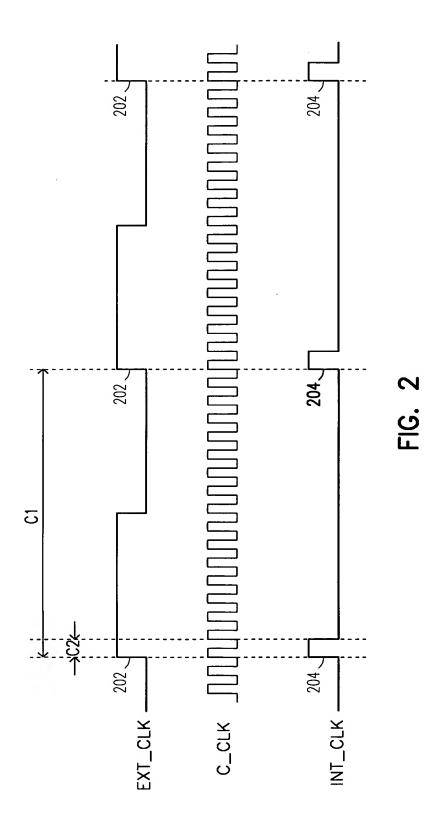
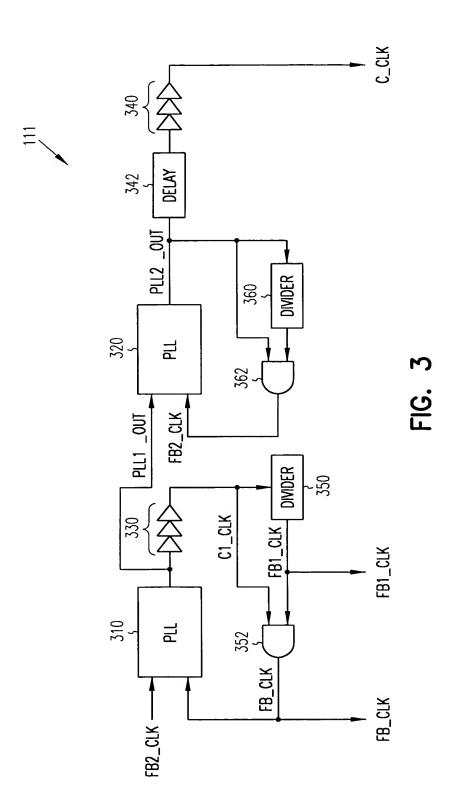
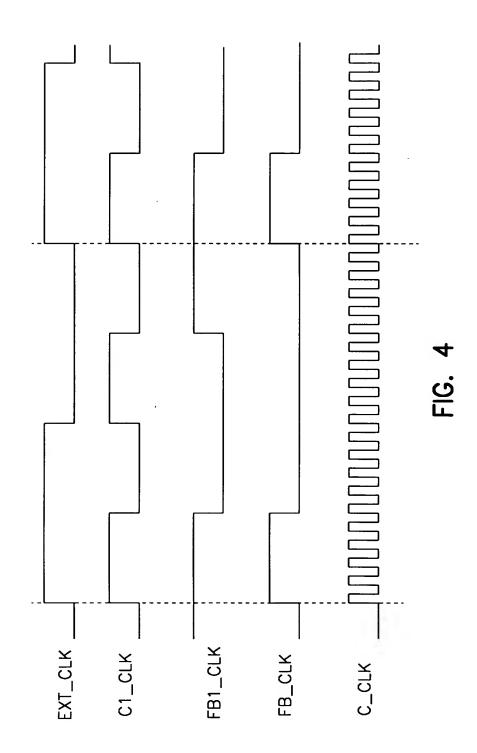


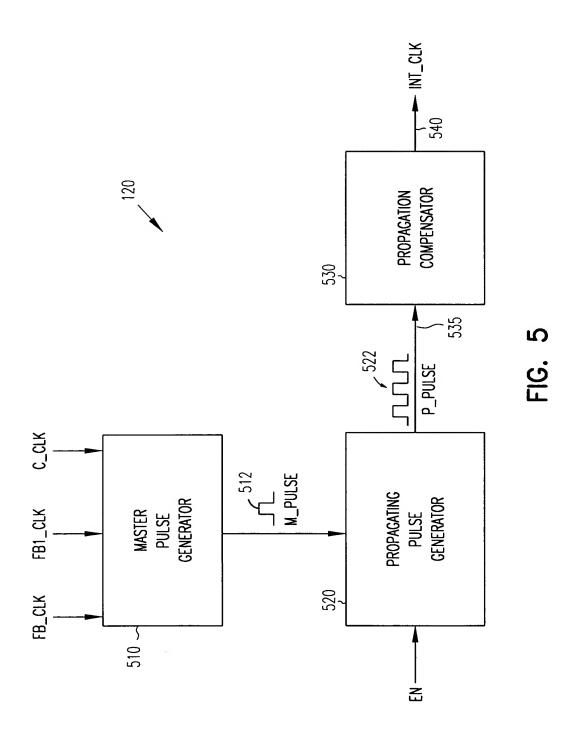
FIG. 1

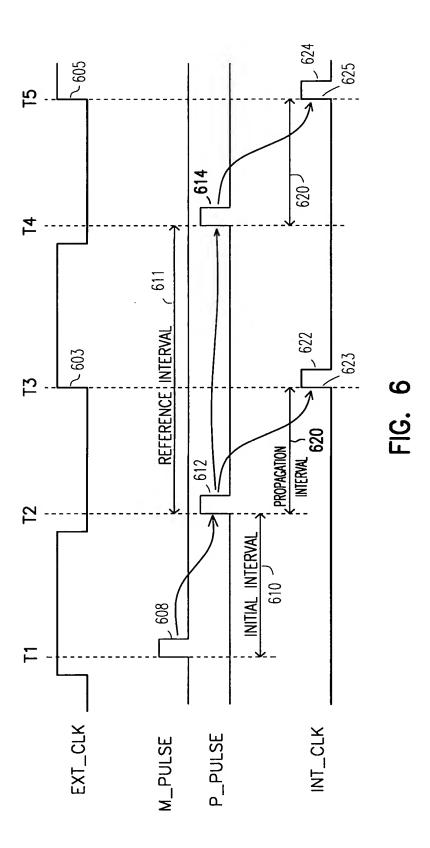


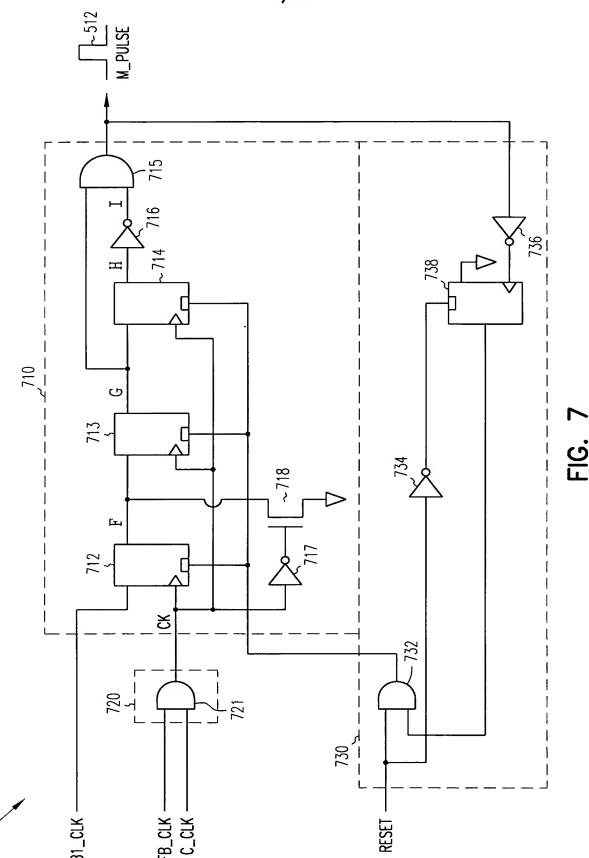
TITLE: CIRCUITS AND METHODS FOR ALIGNMENT OF SIGNALS IN INTEGRATED CIRCUITS INVENTOR NAME: Tanveer R. Khondker et al. SERIAL NO.: 10/612,722

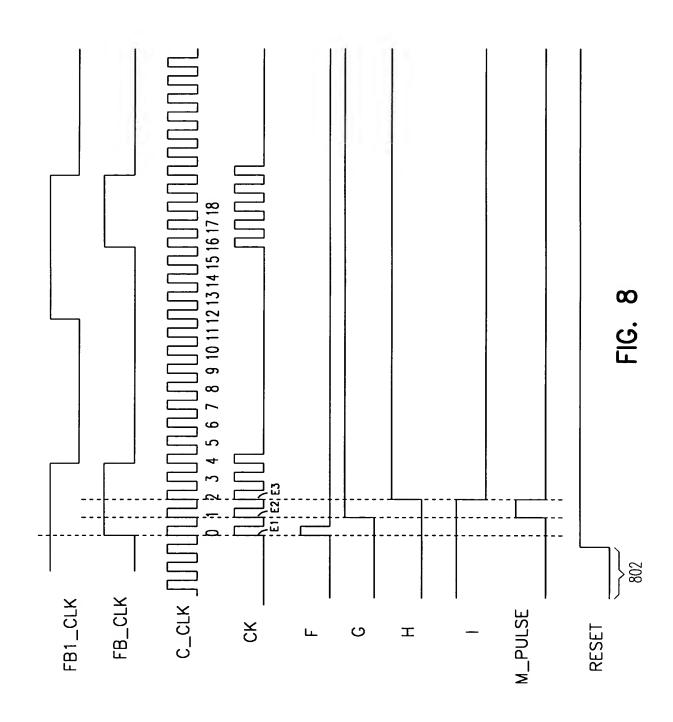


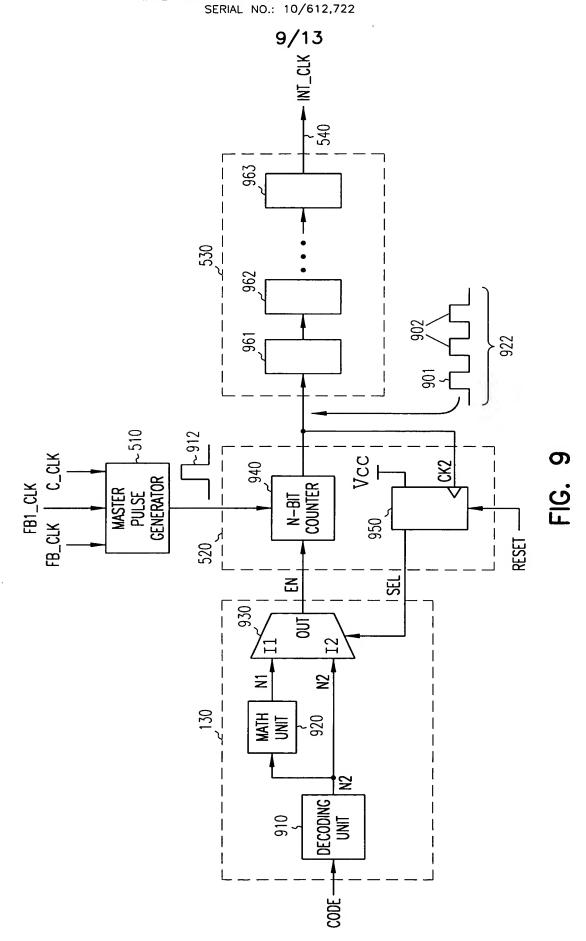




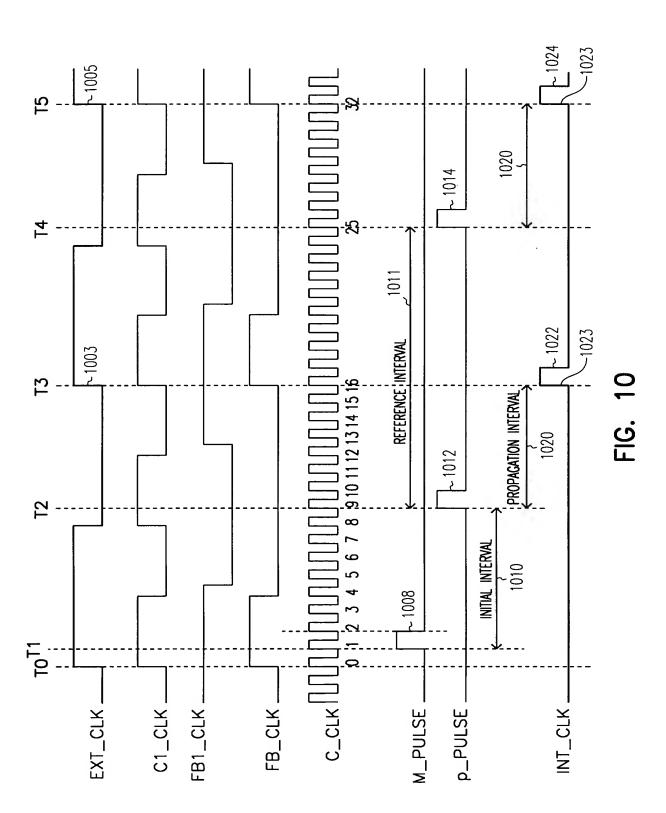


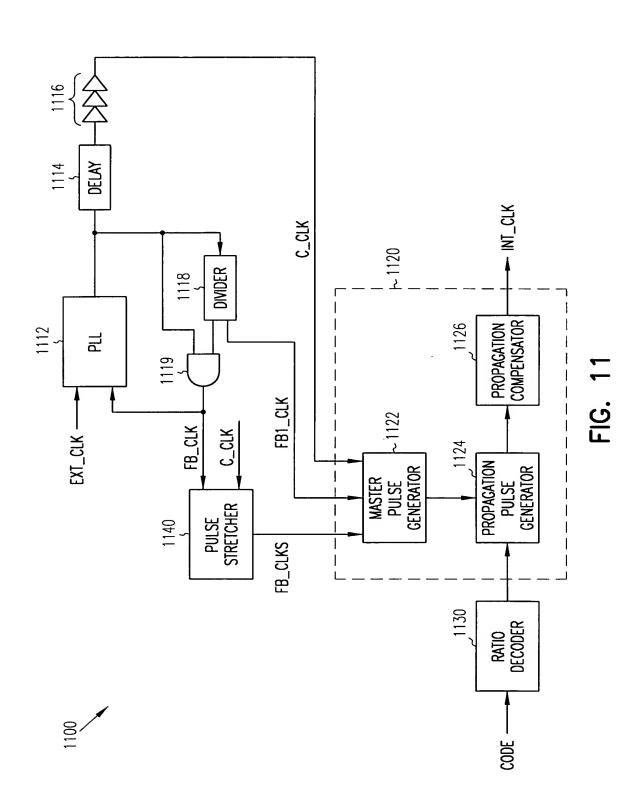






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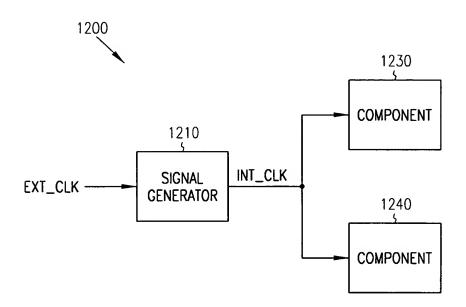


FIG. 12

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